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Fig.1
(PRIOR ART)

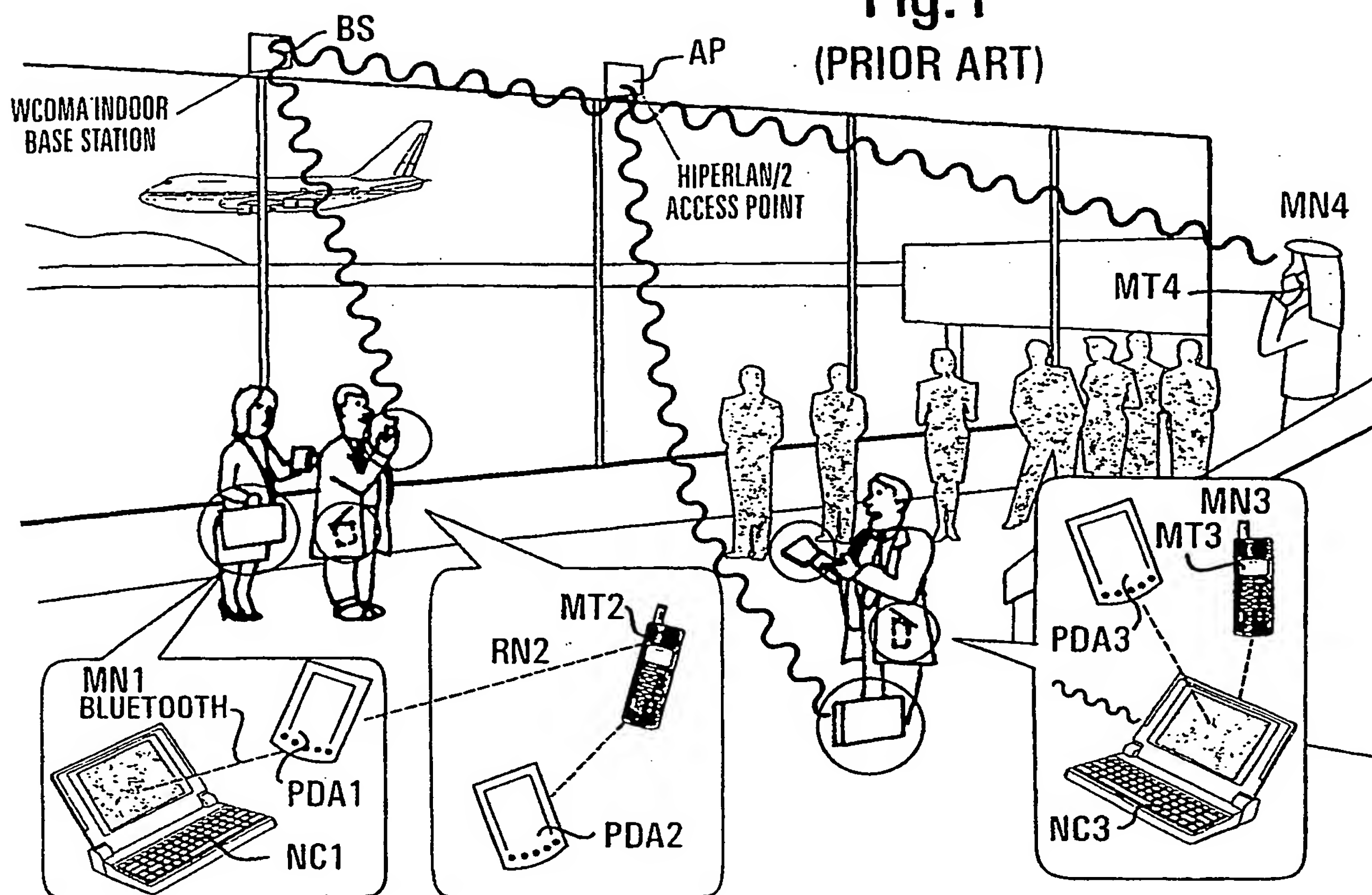
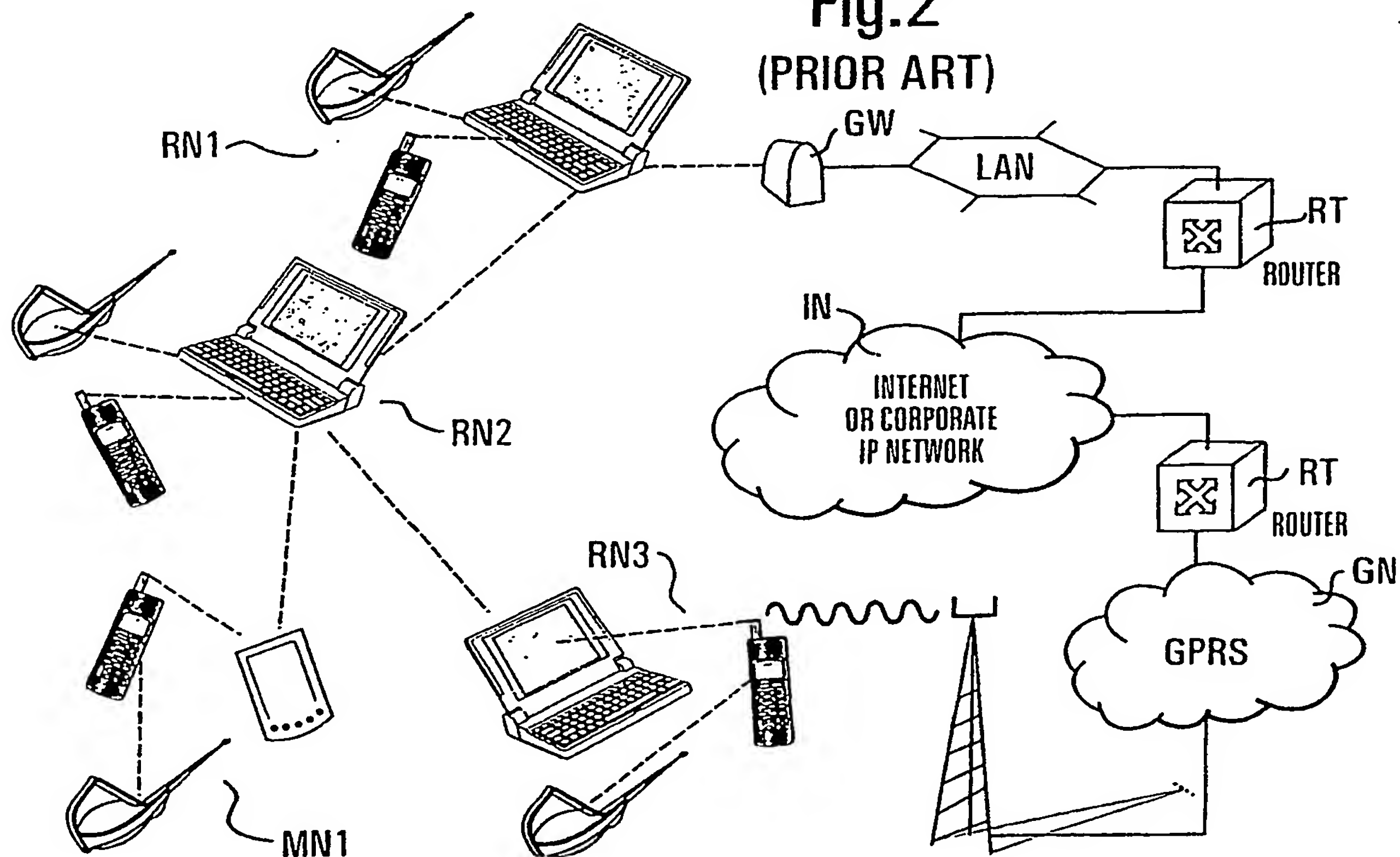


Fig.2
(PRIOR ART)



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Fig. 3a

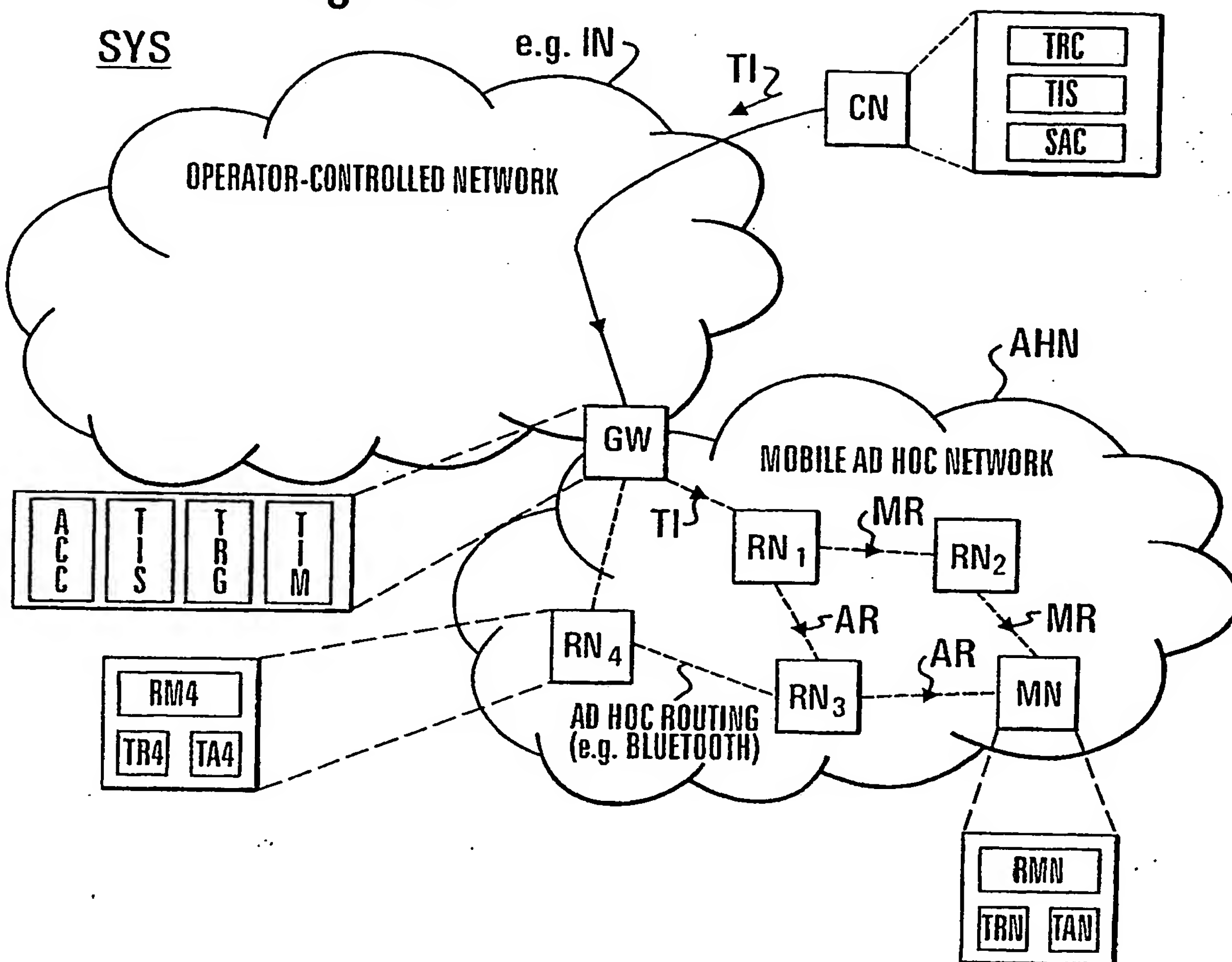


Fig. 3b

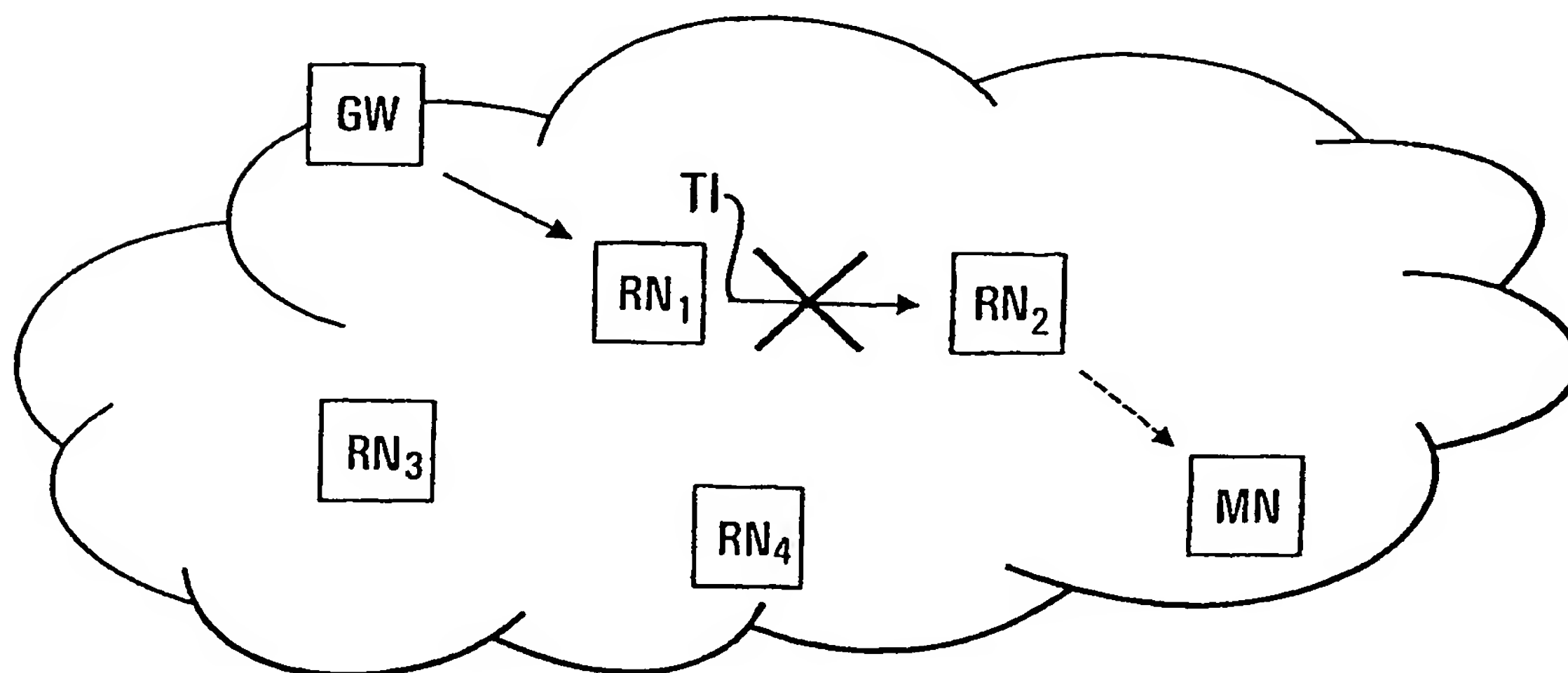


Fig. 4a

TIM

ACC

TIS

SA	DAM	TRT	TA	CH
SAC	2MB	64kbits/s	TA4	7ct
SAC	1MB	128kbits/s	TAN	2ct
.
.
.

TCH

Fig. 4b

TIM

ACC

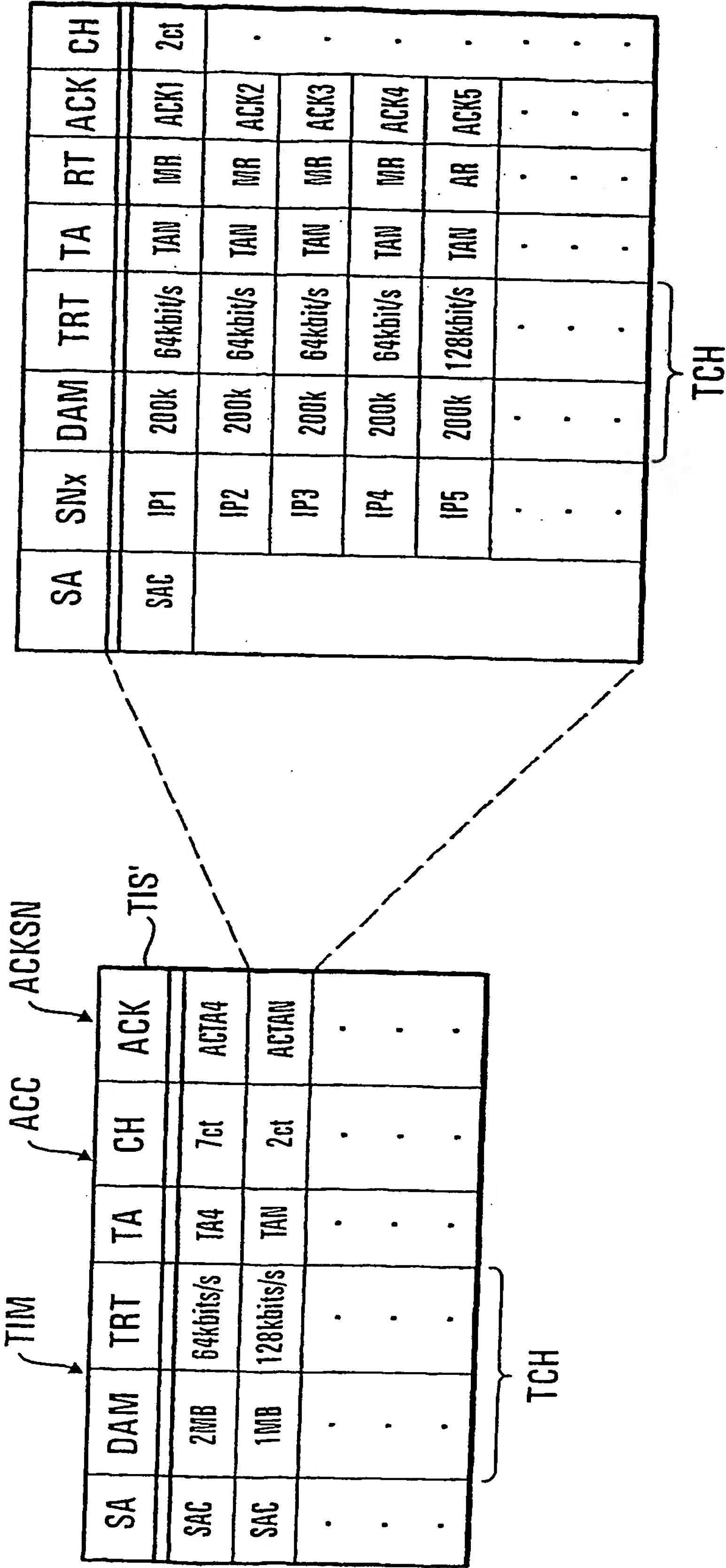
ACKM/ACKS4
ACKSN

TIS'

SA	DAM	TRT	TA	CH	ACK
SAC	2MB	64kbits/s	TA4	7ct	ACTA4
SAC	1MB	128kbits/s	TAN	2ct	ACTAN
.
.
.

TCH

Fig. 4c



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Fig. 5a

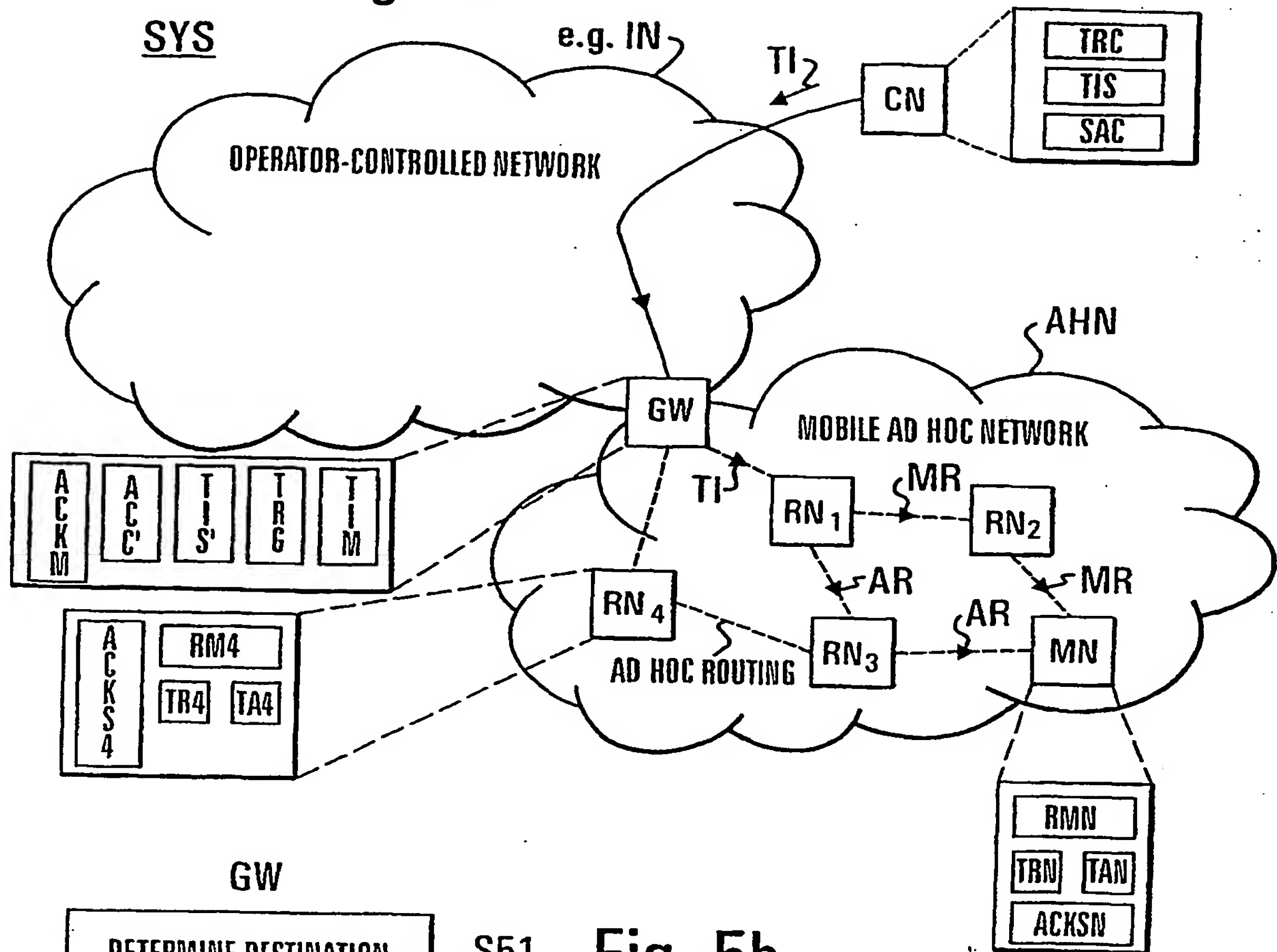


Fig. 5b

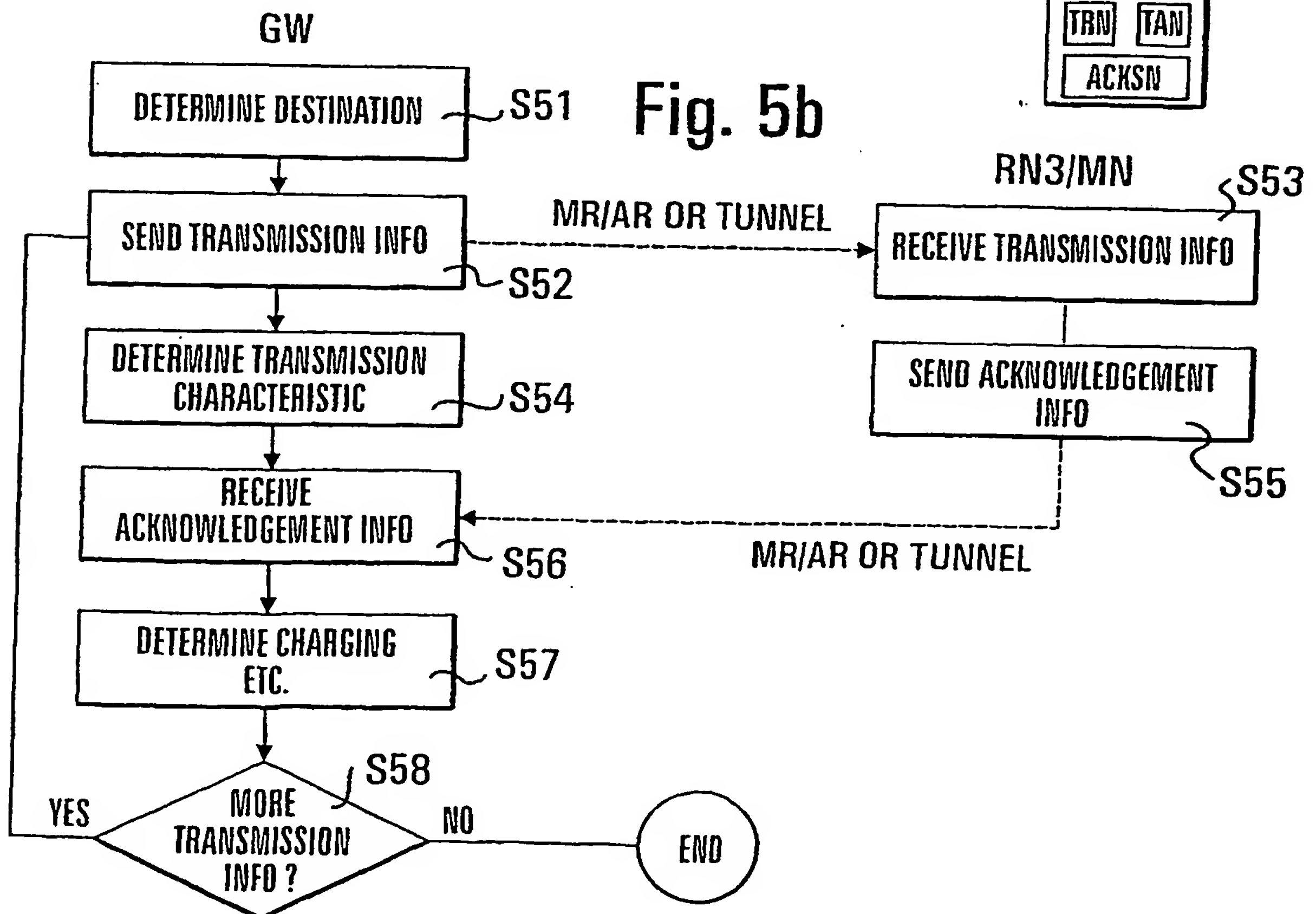


Fig. 5c

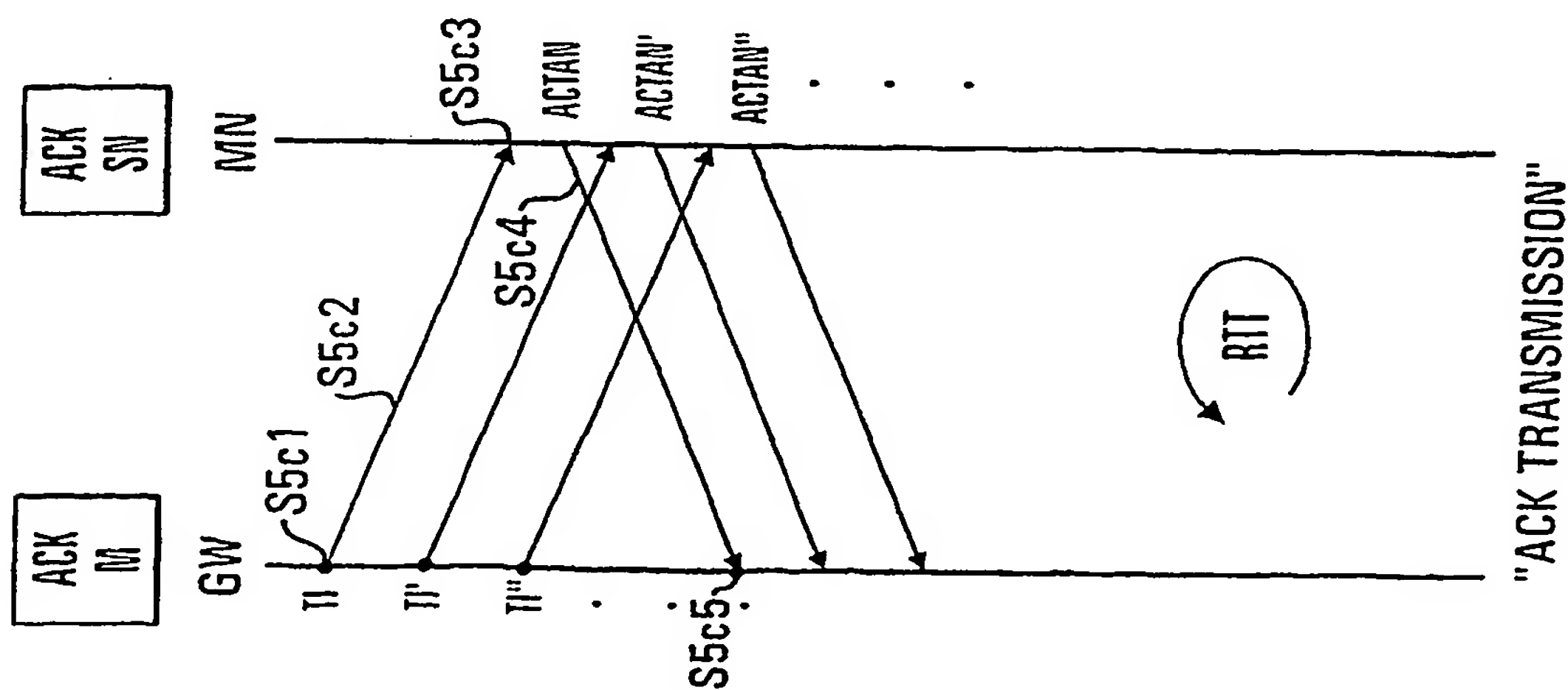


Fig. 7

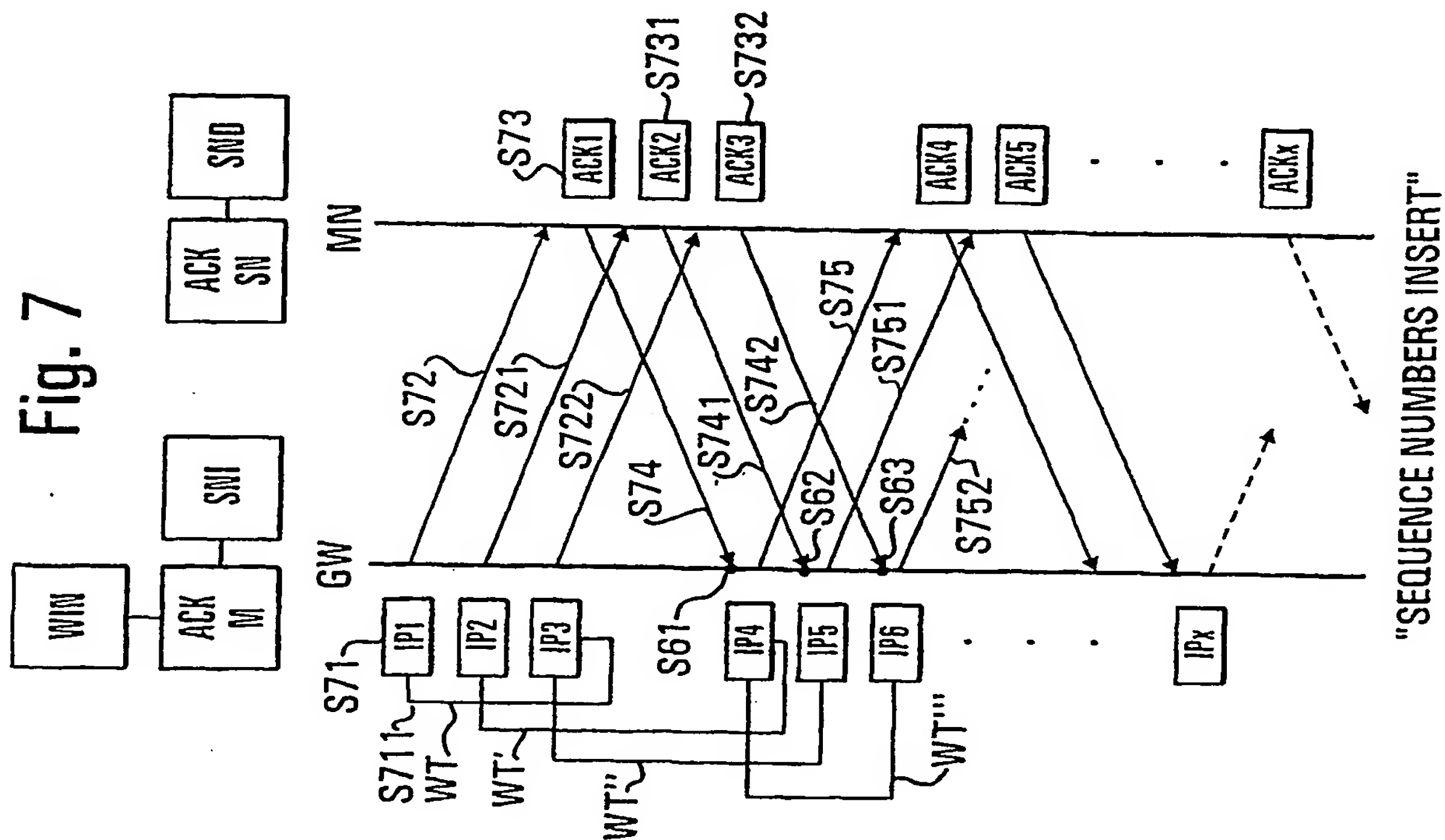


Fig. 8

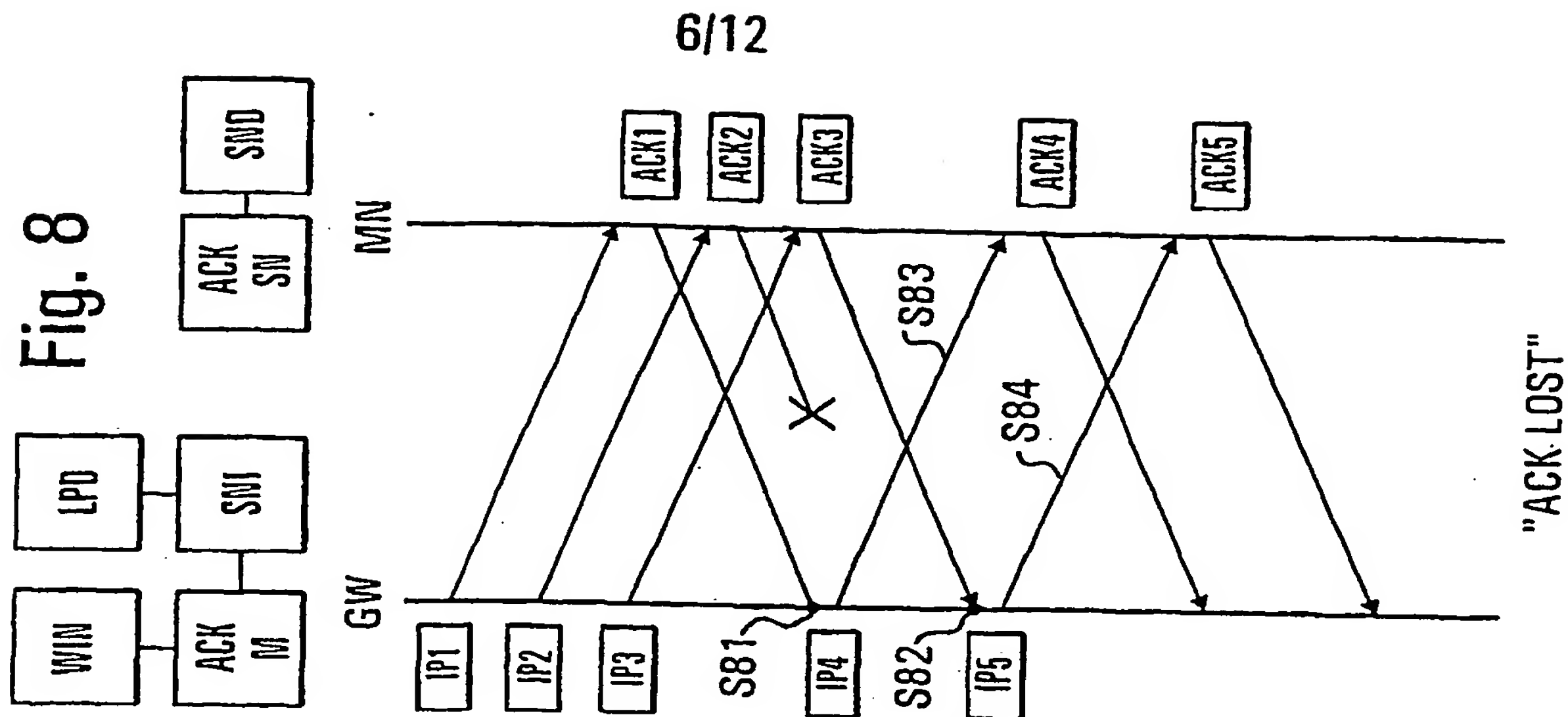
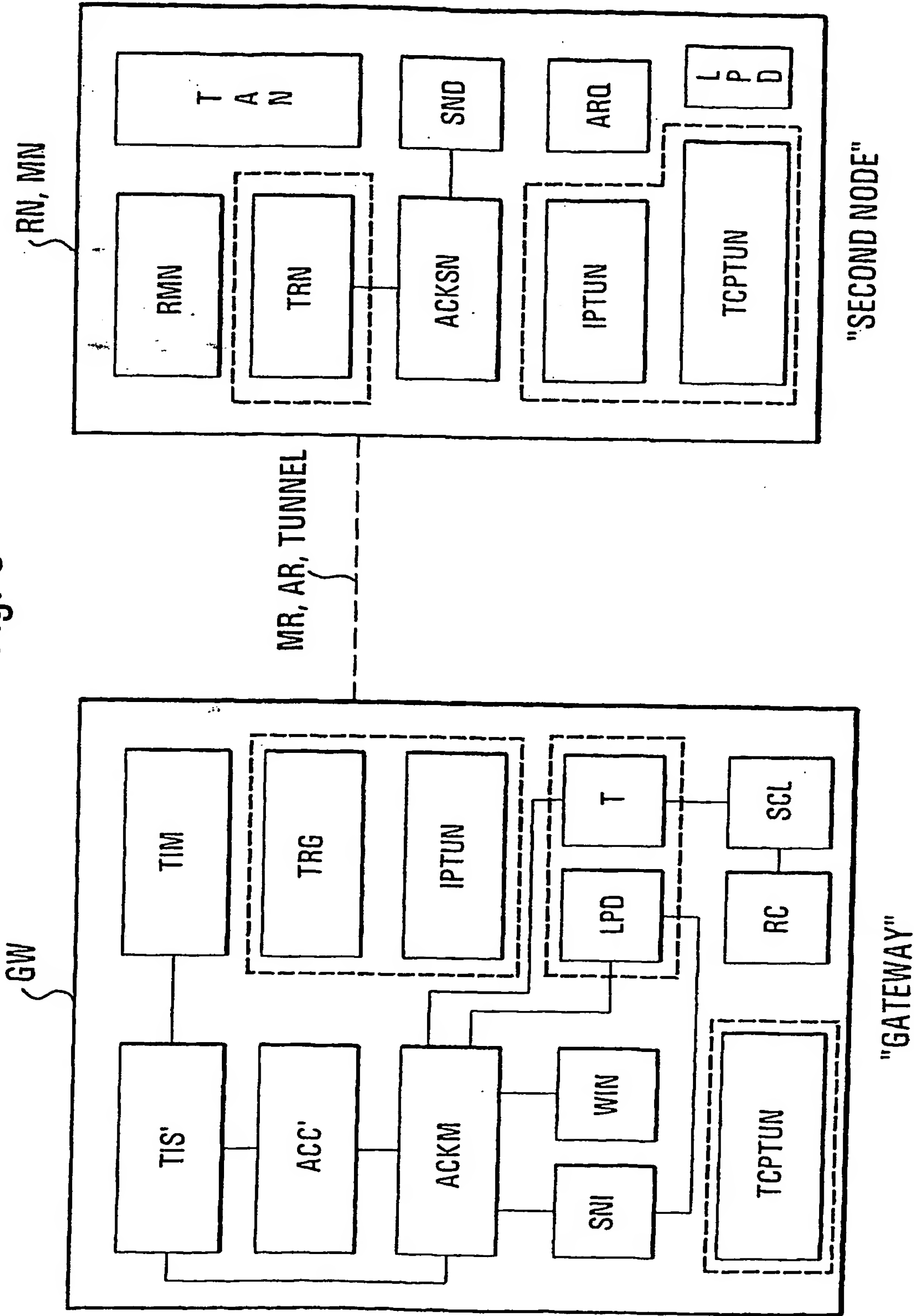
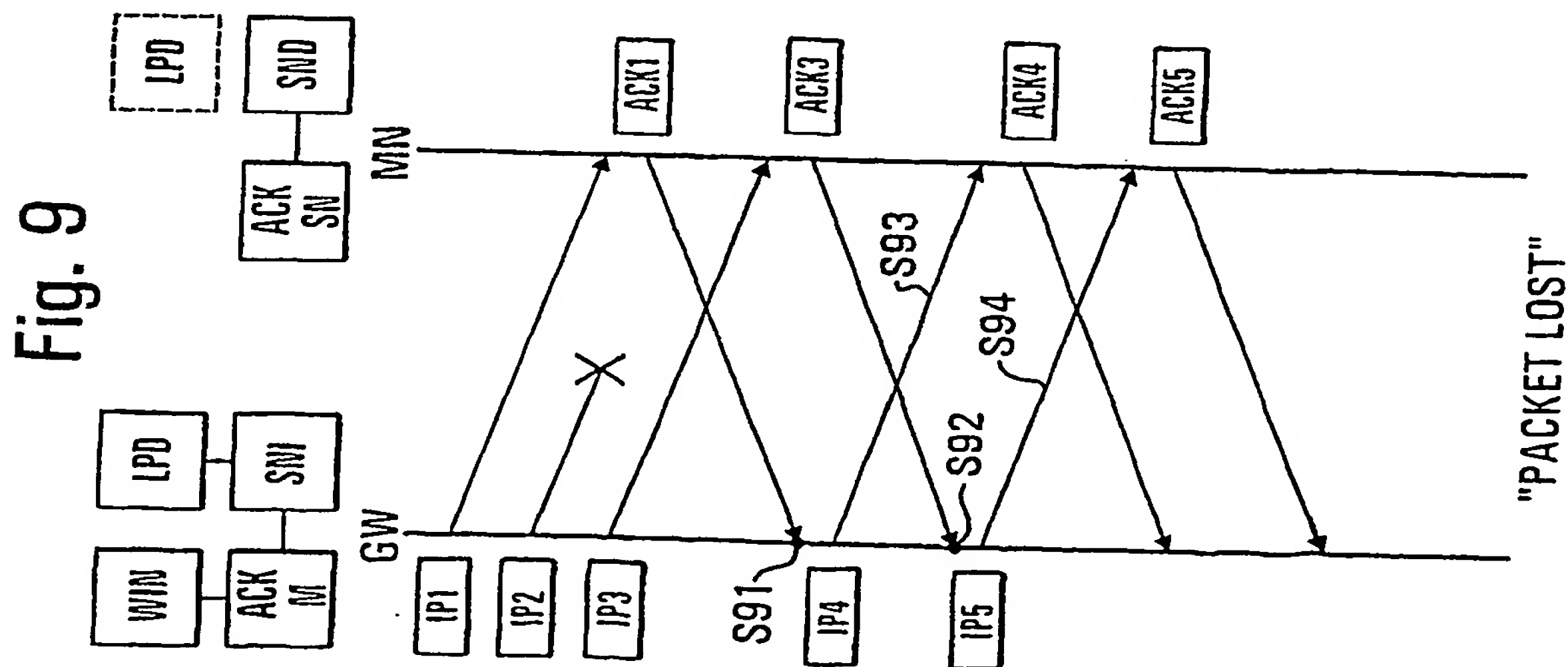
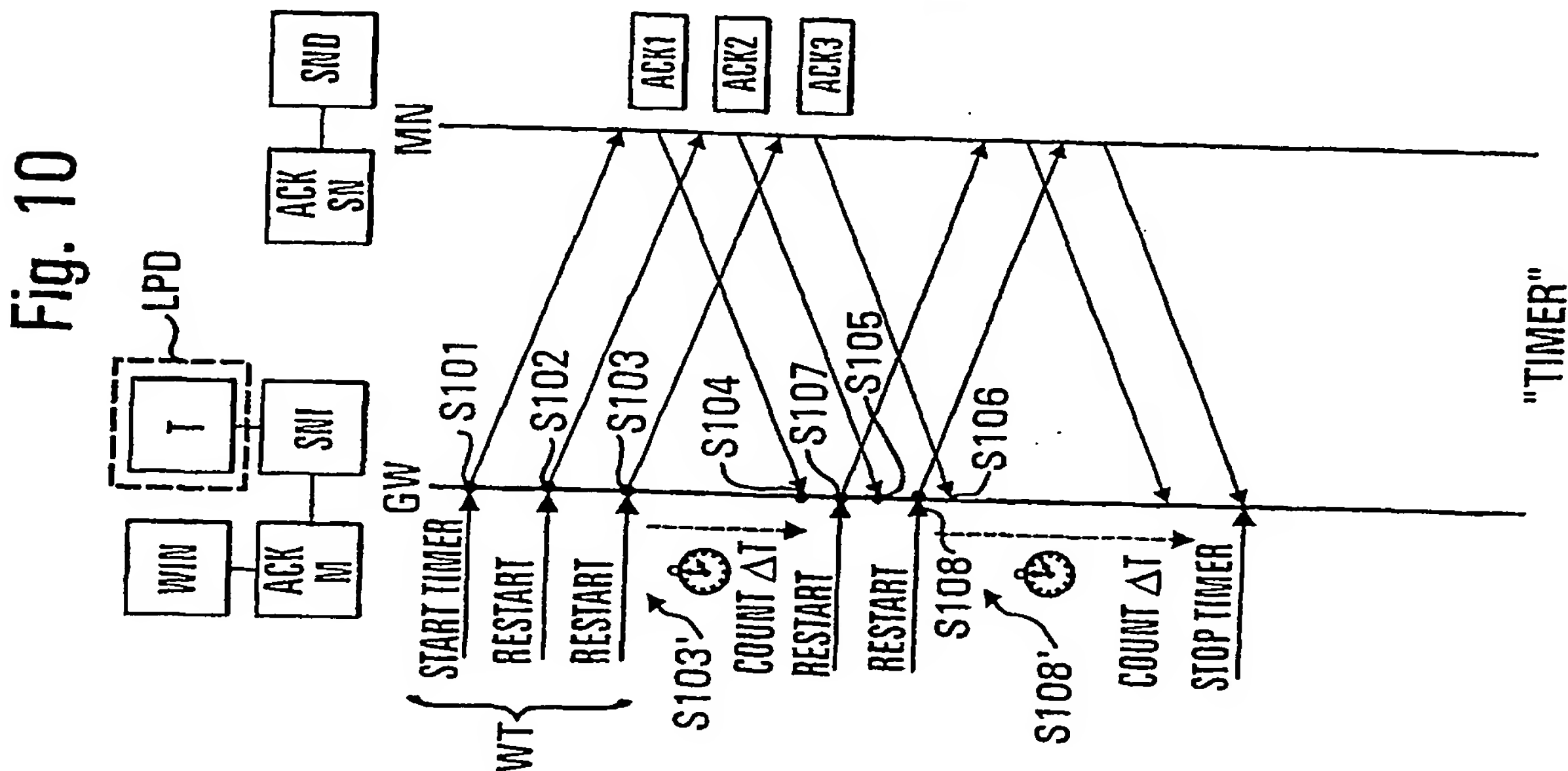
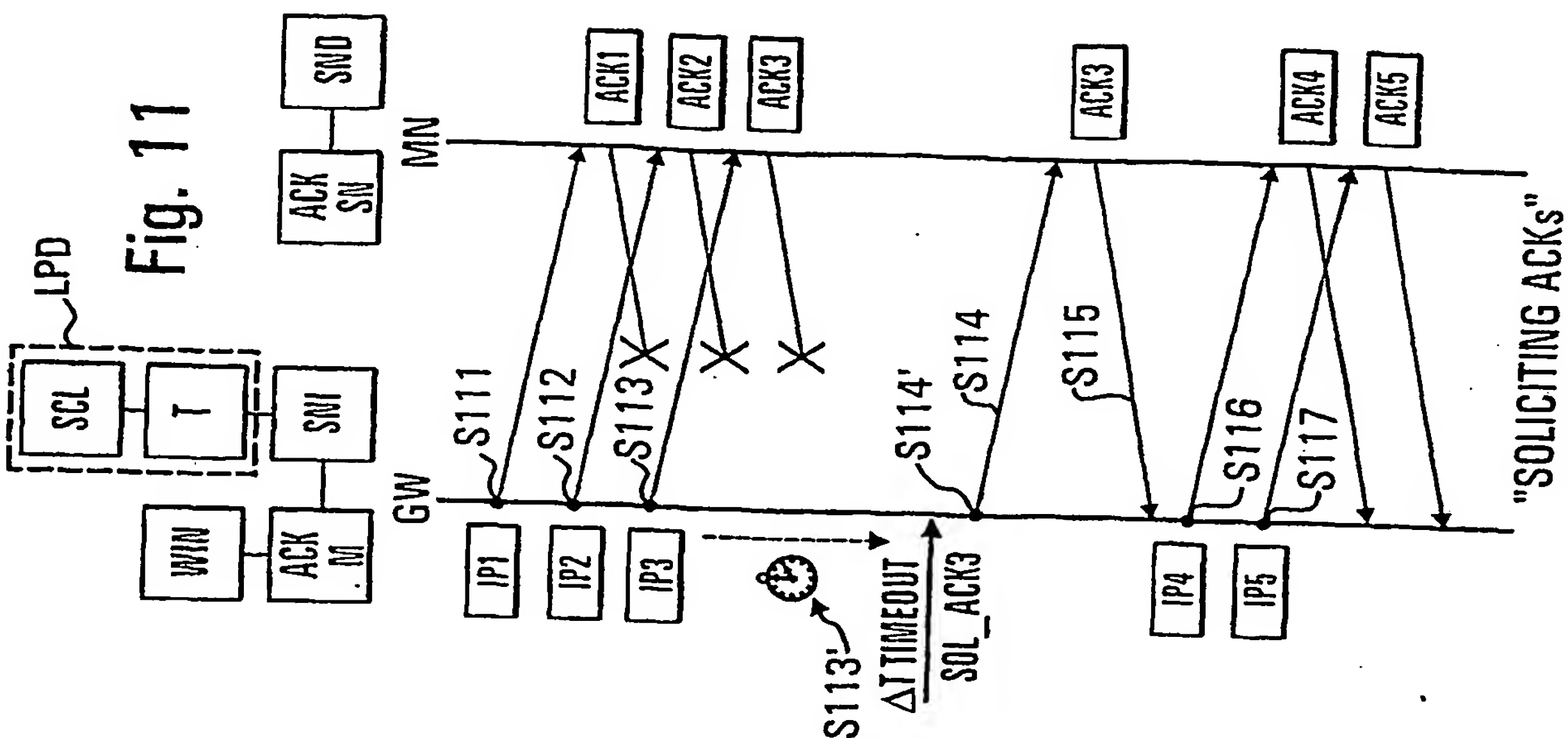


Fig. 6





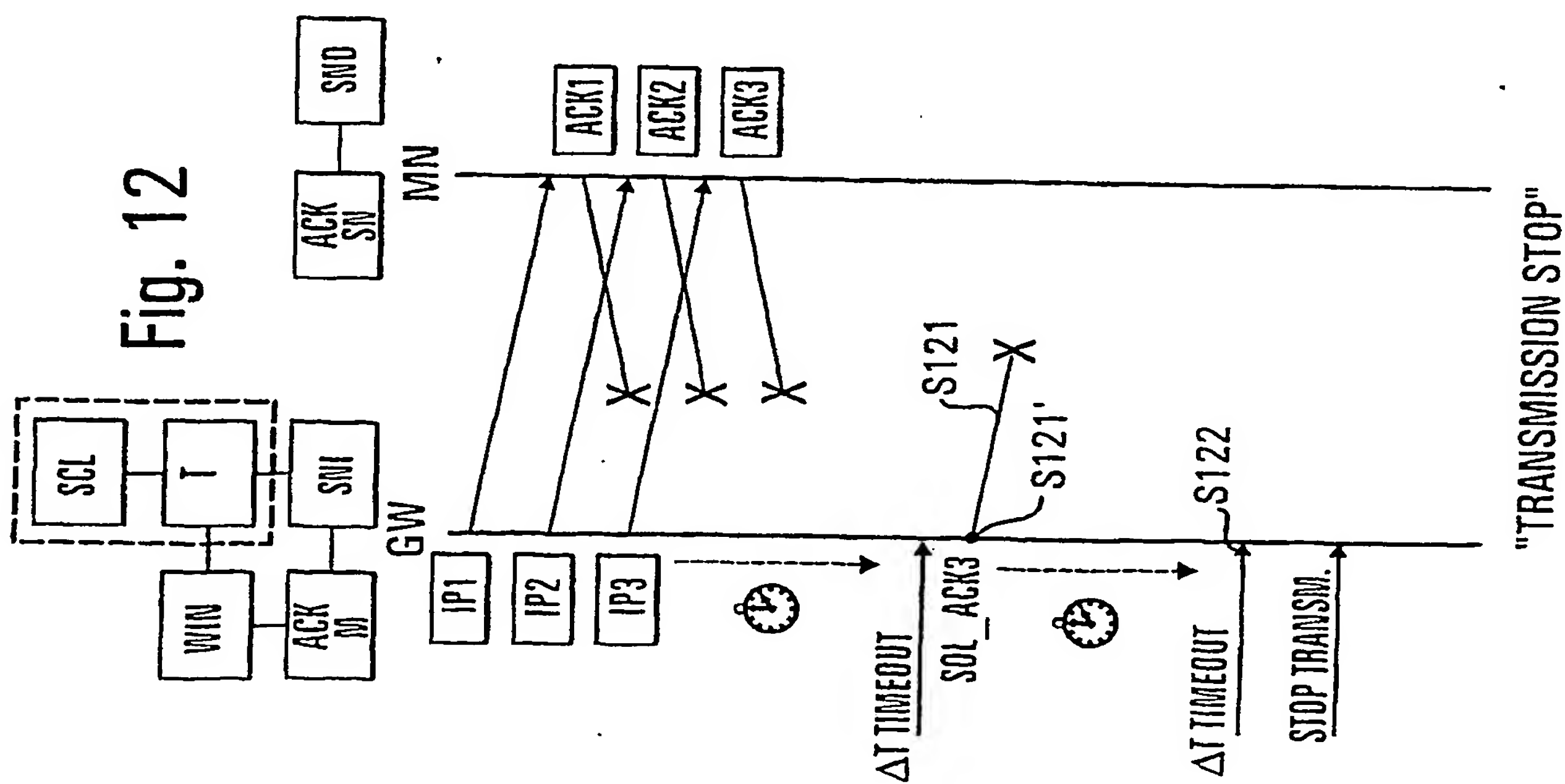
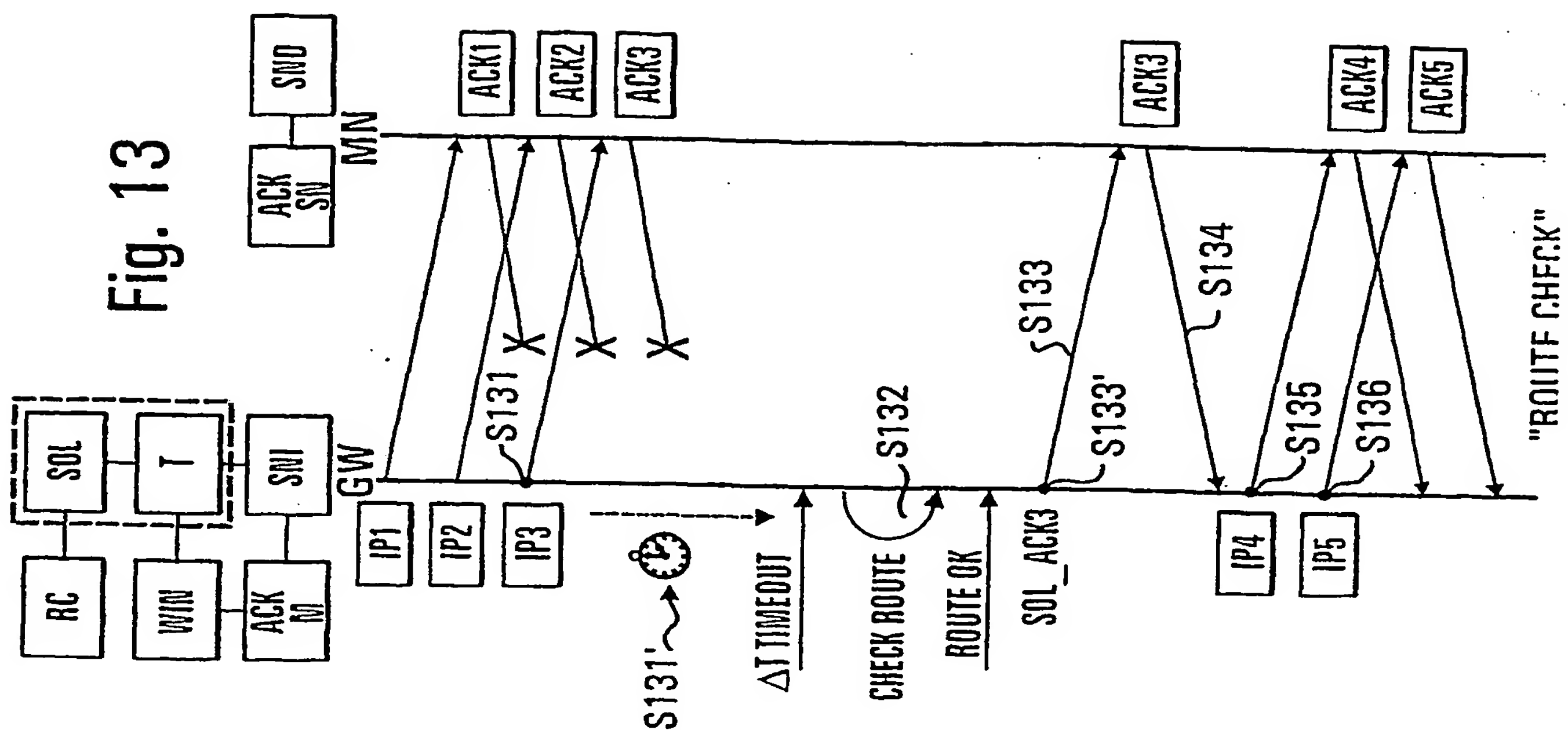


Fig. 15

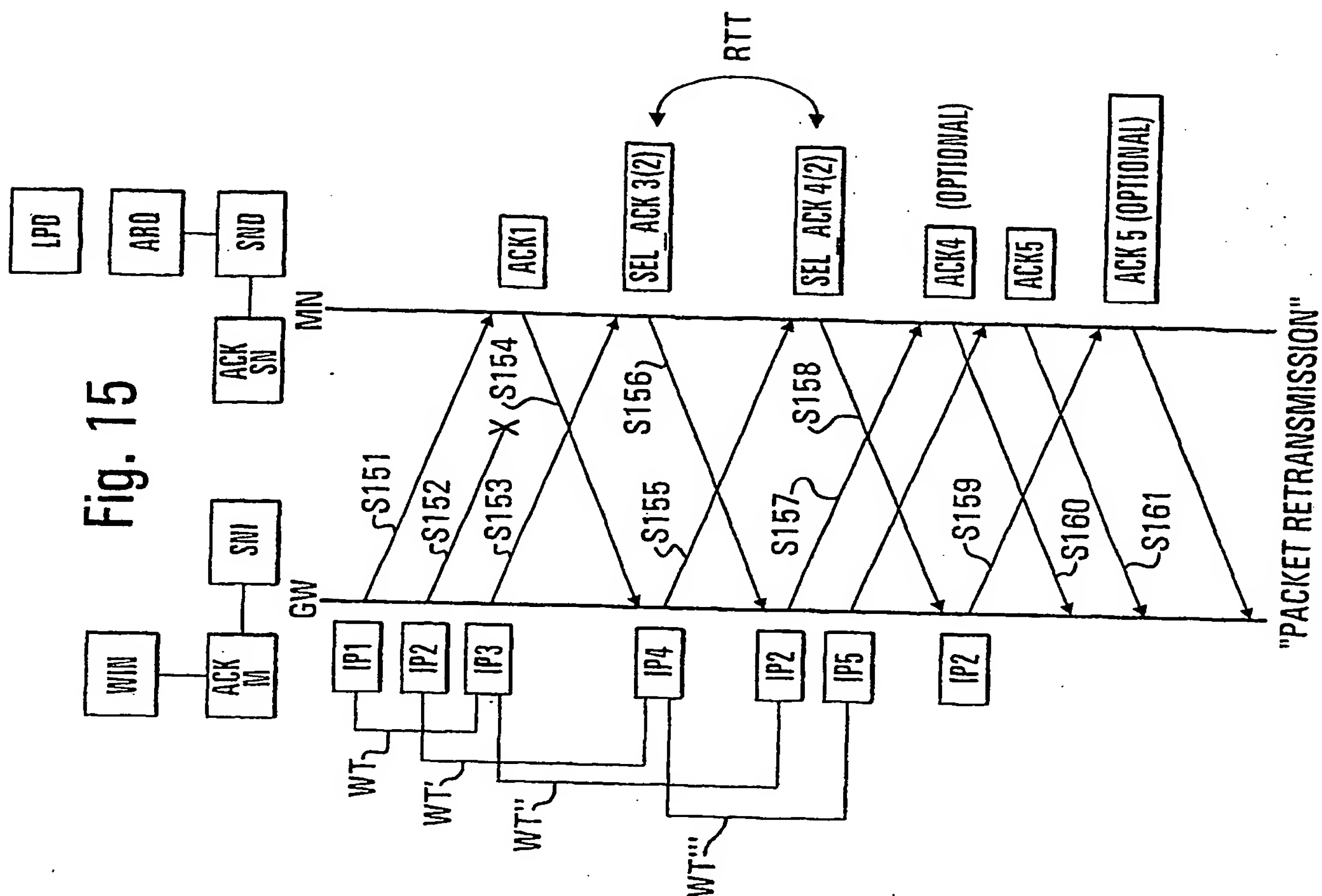


Fig. 14

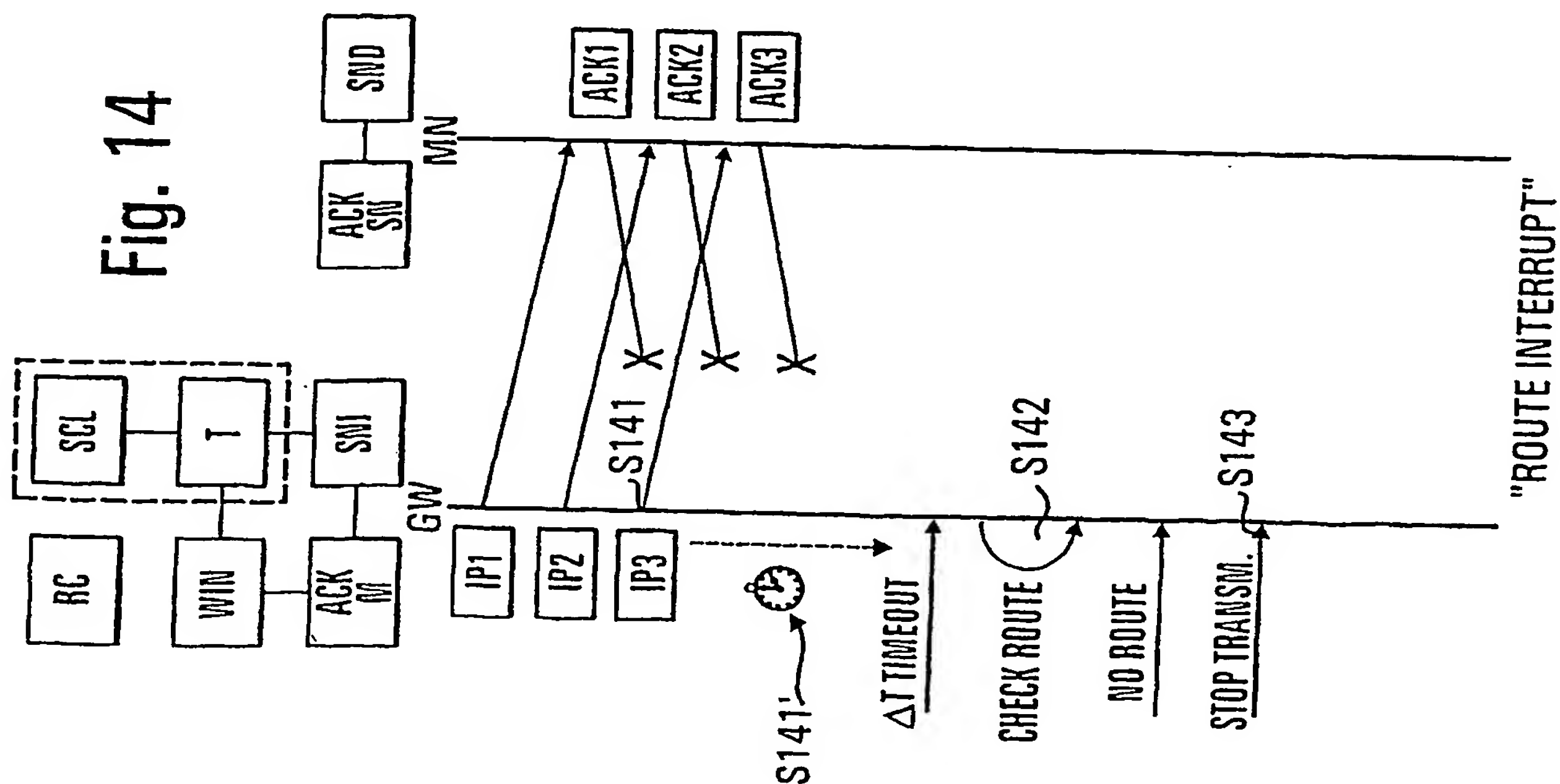


Fig. 16a

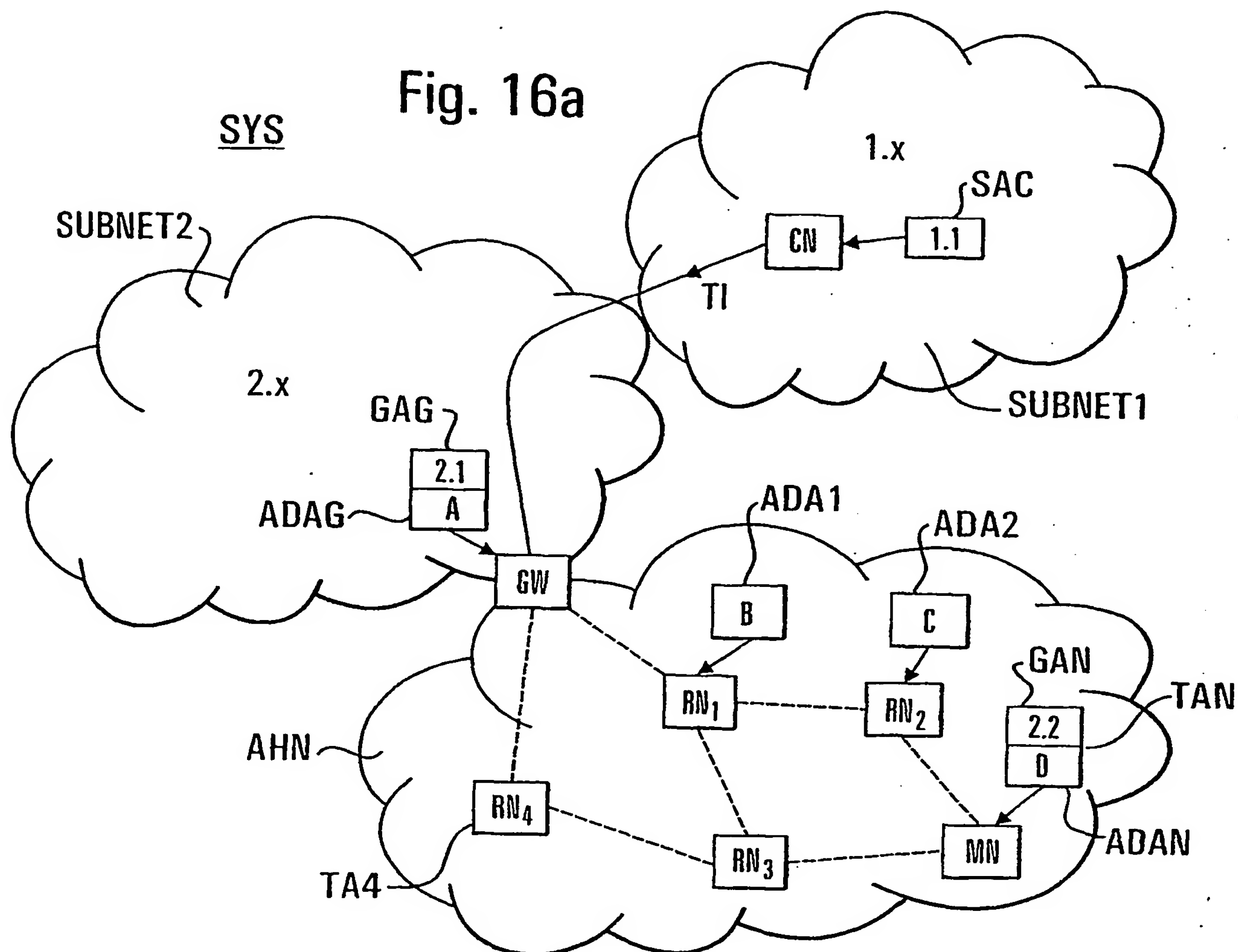


Fig. 16b

